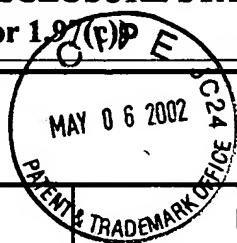


TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
BUR92001017SUS1

In Re Application Of: Timothy Lehner, et al.

Serial No.
10/063,214Filing Date
03/29/02

Examiner

Group Art Unit

Title: A CIRCUIT AND METHOD FOR MODELING I/O

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37 CFR 1.97(b)

- The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

- The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:
 - the statement specified in 37 CFR 1.97(e);
 - OR**
 - the fee set forth in 37 CFR 1.17(p).

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
 (Under 37 CFR 1.97(b) or 1.97(e))

Docket No.
 BUR920010175US1

In Re Application: Timothy Lehner, et al.

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A CIRCUIT AND METHOD FOR MODELING I/O

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(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- A check in the amount of _____ is attached.
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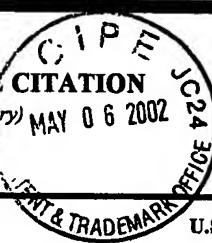
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INFORMATION DISCLOSURE CITATION

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Docket Number (Optional)

BUR920020010175US1

Application Number

10/063,214

Applicant(s)

Timothy Lehner, et al.

Filing Date

03/29/02

Group Art Unit

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		5,481,484	01/02/96	Ogawa et al.			
		5,535,146	07/09/96	Huang et al.			
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REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Bina Ackalloor, Dinesh Gaitonde, "An Overview of Library Characterization in Semi-Custom Design"
5/97 IEEE 1998 Custom Integrated Circuits Conference, pp. 305-312

Jerry D. Hayes and Larry Wissel, "Behavioral Modeling for Timing, Noise, and Signal Integrity Analysis"
IBM Microelectronics Division

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>				ATTY DOCKET NO. BUR920010175US1	SERIAL NO. 10/063,214	
				APPLICANT(S) Timothy Lehner, et al.		
				FILING DATE 03/29/02	GROUP	
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*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS
		6,035,115	03/07/00	Suzuki		
		6,080,201	06/27/00	Hojat, et al.		
		6,102,960	08/15/00	Berman, et al.		
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FOREIGN PATENT DOCUMENTS						
DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
					YES	NO
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)						
		Chandrakasan et al., "Design of High-Performance Microprocessor Circuits" ISBN 0-7803-6001-X, IEEE Order No. PCS836, Chapter 16.3 pp. 338-345				
		Jessica Qian, et al., "Modeling the 'Effective Capacitance' for the RC Interconnect of CMOS Gates" December 1994, Volume 13, No. 12, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems pp. 1526-1535				
EXAMINER				DATE CONSIDERED		
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